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Q. 7.15: Using  
64 \* 8 ROM  
chips with an  
enable input,  
construct a  
512 \* 8 ROM  
with eight  
chips and a

**Q. 7.15: Using  
64 \* 8 ROM  
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**enable input,  
construct a  
512 \* 8 ROM  
with eight  
chips and a by  
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Kakati 6  
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minutes, 17  
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Using 64 \* 8**

**ROM chips with  
an enable  
input,  
construct a  
512 \* 8 ROM  
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**Q. 3.6:**

*Page 24/46*



Simplify the  
following  
Boolean  
expressions,  
using four-  
variable maps:  
(a)  $A'B'C'D' +$   
 $AC'D' +$

**Q. 3.6:**  
**Simplify the  
following**

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**Boolean  
expressions,  
using four-  
variable maps:  
(a)  $A'B'C'D' +$   
 $AC'D' +$  by Dr.  
Dhiman Kakati  
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seconds 19,066  
views Q. 3.6:  
Simplify the**

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**following  
Boolean  
expressions,  
using four-  
variable maps:  
(a)  $A'B'C'D' + AC'D' + B'CD' + A'BCD + BC'D$   
(b)  $x'z \dots$**

**Digital  
Design: Q.**

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1.13: Do the  
following  
conversion  
problems: (a)  
Convert  
decimal 27.315  
to binary

**Digital  
Design: Q.  
1.13: Do the  
following**

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**conversion  
problems: (a)  
Convert  
decimal 27.315  
to binary by  
Dr. Dhiman  
Kakati 1 year  
ago 7 minutes,  
40 seconds  
4,479 views Q.  
1.13: Do the  
following**

**conversion  
problems: (a)  
Convert  
decimal 27.315  
to binary. (b)  
Calculate the  
binary  
equivalent of  
 $2/3$  out ...**

**Q. 3.23.  
Implement the**

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following  
Boolean  
function F,  
together with  
the don't-care  
conditions d

**Q. 3.23.**  
**Implement the**  
**following**  
**Boolean**  
**function F,**

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**together with  
the don't-care  
conditions d  
by Dr. Dhiman  
Kakati 1 year  
ago 6 minutes,  
46 seconds  
3,635 views Q.  
3.23.**

**Implement the  
following  
Boolean**



**function  $F$ ,  
together with  
the don't-care  
conditions  $d$ ,  
using no more  
than two NOR  
gates: ...**

**Q. 4.7: Design  
a  
combinational  
circuit that**

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converts a  
four-bit Gray  
code (Table  
1.6) to a bit  
four-

**Q. 4.7: Design  
a  
combinational  
circuit that  
converts a  
four-bit Gray**

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code (Table  
1.6) to a bit  
four- by Dr.  
Dhiman Kakati  
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10 minutes, 28  
seconds 7,965  
views Q. 4.7:  
, Design , a  
combinational  
circuit that  
converts a

**four-bit Gray  
code (Table  
1.6) to a bit  
four- binary  
number. (a)  
Implement  
the ...**

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**[Q. 4.9: An ABC](#)**

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D-to-seven-  
segment  
decoder is a  
combinational  
circuit that  
converts a  
decimal digit  
in

**Q. 4.9: An ABC  
D-to-seven-  
segment**

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**decoder is a  
combinational  
circuit that  
converts a  
decimal digit  
in by Dr.**

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**Q. 4.9: An ABC  
D-to-seven-**

**segment decoder is a combinational circuit that converts a decimal digit in BCD to an appropriate code . . .**

**[Q. 6.12: Draw the logic](#)**



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diagram of a  
four-bit  
binary ripple  
countdown  
counter  
using(a)  
flip-flops

**Q. 6.12: Draw  
the logic  
diagram of a  
four-bit**

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countdown  
counter  
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**Q. 6.12: Draw  
the logic  
diagram of a  
four-bit  
binary ripple  
countdown  
counter ...**

**Q. 6.10:  
Design a  
serial 2's**

complementer  
with a shift  
register and a  
flip-flop. The  
binary number

**Q. 6.10:**  
**Design a**  
**serial 2's**  
**complementer**  
**with a shift**  
**register and a**

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**flip-flop. The  
binary number  
by Dr. Dhiman  
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serial 2's  
complementer  
with a shift  
register and a  
flip-flop.**

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